# iM7131

# N-Channel Enhancement Mode MOSFET

## Datasheet

## 1. Product Information

### 1.1 Features

- Advanced Trench Cell Design
- RoHS Compliant and Halogen Free
- Reliable and Rugged
- Moisture Sensitivity Level MSL1



### 1.2 Package Type

TOLLB-8L

### 1.3 Quick Reference

| Symbol | Parameter | Conditions | Min | Typ | Max | Unit |
|---|---|---|---|---|---|---|
| **Limiting Values** | | | | | | |
| $P_{tot}$ | Total Power Dissipation | $T_C$ = 25 ℃ | - | - | 300 | W |
| $I_{DS}$ | Drain-Source Current | $T_C$ = 25 ℃, $V_{GS}$ = 10 V | - | - | 277 | A |
| | | $T_C$ = 100 ℃, $V_{GS}$ = 10 V | - | - | 196 | A |
| **Static Characteristics** | | | | | | |
| $BV_{DSS}$ | Drain-Source Breakdown Voltage | $V_{GS}$ = 0 V, $I_{DS}$ = 250 μA | 100 | - | - | V |
| $V_{GS(th)}$ | Gate Threshold Voltage | $V_{DS}$ = $V_{GS}$, $I_{DS}$ = 250 μA | 2 | - | 4 | V |
| $R_{DS(ON)}$ | Drain-Source On-State Resistance | $V_{GS}$ = 10 V, $I_{DS}$ = 50 A | - | 1.45 | 1.8 | mΩ |
| | | $V_{GS}$ = 6 V, $I_{DS}$ = 30 A | - | 2.3 | 3.1 | |

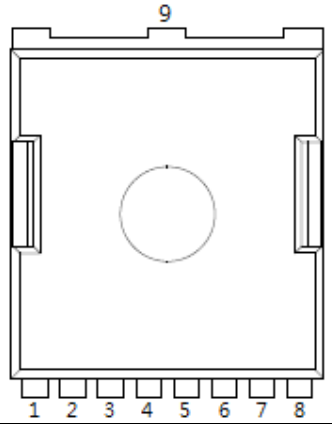
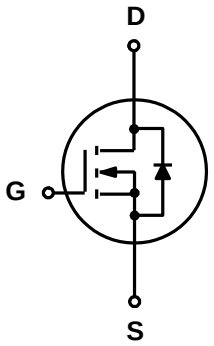
## 2. Ordering Code & Marking Information

| Ordering Code | Marking Information |
|---|---|
| iM7131 | iM7131<br>XXXXX<br>X: Date Code |



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## 3. Pin Description

| Pin | Description | Simplified Outline | Symbol |
|---|---|---|---|
| 1 | Gate(G) |  |  |
| 2~8 | Source(S) | | |
| 9 | Drain(D) | | |

## 4. Limiting Values

| Symbol | Parameter | | Rating | Unit |
|---|---|---|---|---|
| $V_{DS}$ | Drain-Source Voltage | | 100 | V |
| $V_{GS}$ | Gate-Source Voltage | | ± 20 | V |
| $I_{DS}$ | Drain-Source Current | $T_C$ = 25 ℃, $V_{GS}$ = 10 V | 277 | A |
| | | $T_C$ = 100 ℃, $V_{GS}$ = 10 V | 196 | A |
| $I_{DM}$ Note1 | Pulsed Drain-Source Current | $T_C$ = 25 ℃, $V_{GS}$ = 10 V | 1108 | A |
| $I_S$ | Diode Forward Current | $T_C$ = 25 ℃, $V_{GS}$ = 0 V | 277 | A |
| $E_{AS}$ | Single Pulsed Avalanche Energy | $V_{DD}$ = 50 V, L = 1.0 mH | 2450 | mJ |
| $P_{tot}$ | Total Power Dissipation | $T_C$ = 25 ℃ | 300 | W |
| $R_{\theta JA}$ Note2 | Thermal Resistance- Junction to Ambient | | 44 | ℃ / W |
| $R_{\theta JC}$ | Thermal Resistance- Junction to Case | | 0.5 | ℃ / W |
| $T_{stg}$ | Storage Temperature | | - 55 ~ 175 | ℃ |
| $T_J$ | Junction Temperature | | 175 | ℃ |


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## 5. Electrical Characteristics ($T_A$ = 25 °C Unless Otherwise Noted)

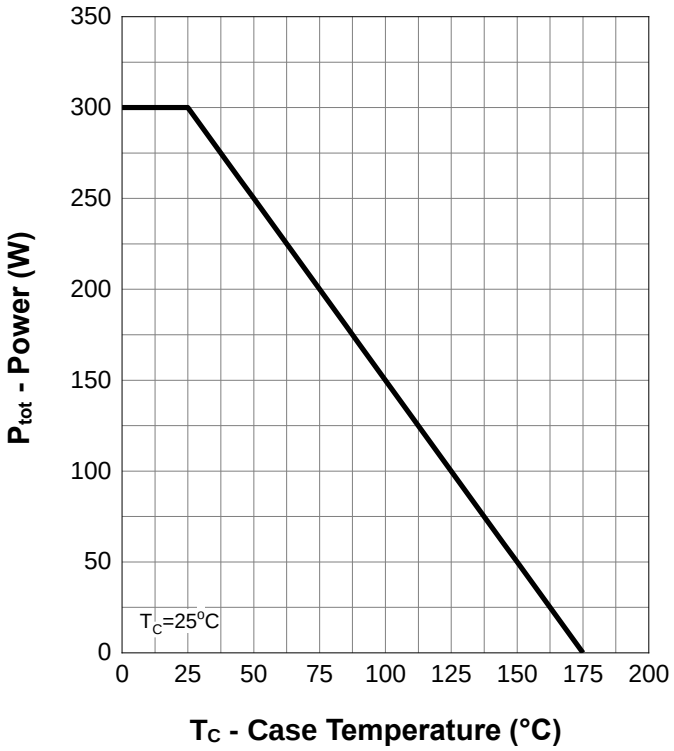
| Symbol | Parameter | Conditions | Min | Typ | Max | Unit |
|---|---|---|---|---|---|---|
| Static Characteristics | | | | | | |
| $BV_{DSS}$ | Drain-Source Breakdown Voltage | $V_{GS}$ = 0 V, $I_{DS}$ = 250 μA | 100 | - | - | V |
| $V_{GS(th)}$ | Gate Threshold Voltage | $V_{DS}$ = $V_{GS}$, $I_{DS}$ = 250 μA | 2 | - | 4 | V |
| $I_{DSS}$ | Drain Leakage Current | $V_{DS}$ = 80 V, $V_{GS}$ = 0 V | - | - | 1 | μA |
| $I_{GSS}$ | Gate Leakage Current | $V_{DS}$ = 0 V, $V_{GS}$ = ± 20 V | - | - | ± 100 | nA |
| $R_{DS(ON)}$ | Drain-Source On-State Resistance | $V_{GS}$ = 10 V, $I_{DS}$ = 50 A | - | 1.45 | 1.8 | mΩ |
| | | $V_{GS}$ = 6 V, $I_{DS}$ = 30 A | - | 2.3 | 3.1 | |
| Diode Characteristics | | | | | | |
| $V_{SD}$ | Diode Forward Voltage | $I_{SD}$ = 50 A, $V_{GS}$ = 0 V | - | - | 1.3 | V |
| $t_{rr}$ Note3 | Reverse Recovery Time | $I_{DS}$ = 50 A, $V_{GS}$ = 0 V | - | 104 | - | nS |
| $Q_{rr}$ Note3 | Reverse Recovery Charge | $dI_{SD}/dt$ = 100 A/μs | - | 346 | - | nC |
| Dynamic Characteristics Note3 | | | | | | |
| $C_{iss}$ | Input Capacitance | $V_{GS}$ = 0 V, $V_{DS}$ = 50 V<br>Frequency = 1 MHz | - | 6643 | - | pF |
| $C_{oss}$ | Output Capacitance | | - | 2005 | - | |
| $C_{rss}$ | Reverse Transfer Capacitance | | - | 17 | - | |
| $R_{g(int)}$ | Internal Gate Resistance | $V_{GS}$ = 0 V, $V_{DS}$ = 0 V, f = 1 MHz | - | 2 | - | Ω |
| $t_d$(on) | Turn-on Delay Time | $V_{DS}$ = 50 V, $V_{GEN}$ = 10 V,<br>$R_{g(ext)}$ = 3.9 Ω, $R_L$ = 1 Ω,<br>$I_{DS}$ = 50 A | - | 22 | - | nS |
| $t_r$ | Turn-on Rise Time | | - | 73 | - | |
| $t_d$(off) | Turn-off Delay Time | | - | 62 | - | |
| $t_f$ | Turn-off Fall Time | | - | 58 | - | |
| Gate Charge Characteristics Note3 | | | | | | |
| $Q_g$ | Total Gate Charge | $V_{DS}$ = 50 V, $V_{GS}$ = 10 V,<br>$I_{DS}$ = 50 A | - | 96 | - | nC |
| $Q_{gs}$ | Gate-Source Charge | | - | 39 | - | |
| $Q_{gd}$ | Gate-Drain Charge | | - | 16 | - | |

Note 1：Pulse test; pulse width ≤ 300 μs, duty cycle ≤ 2%.
Note 2：Surface Mounted on 1 $in^2$ pad area, t ≤ 10 sec.
Note 3：Guaranteed by design, not subject to production testing.

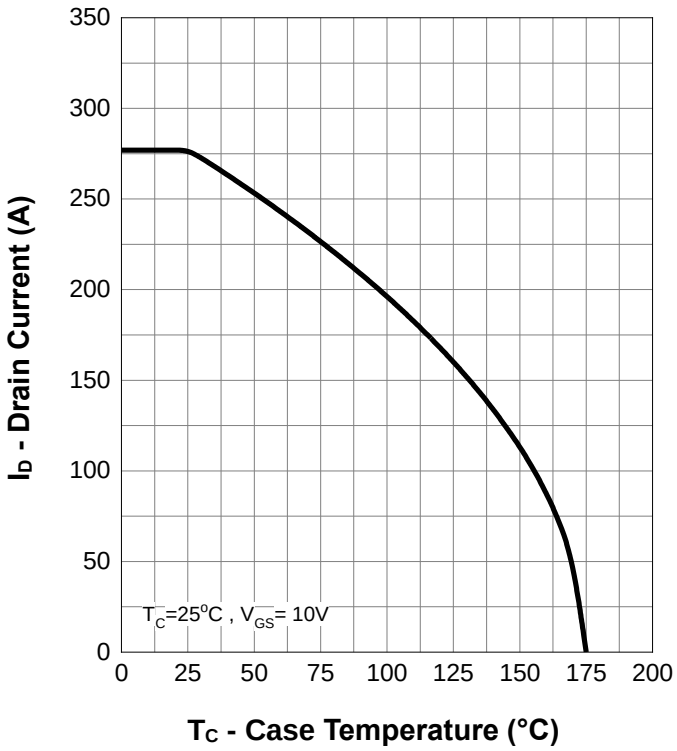

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## 6. Typical Characteristics

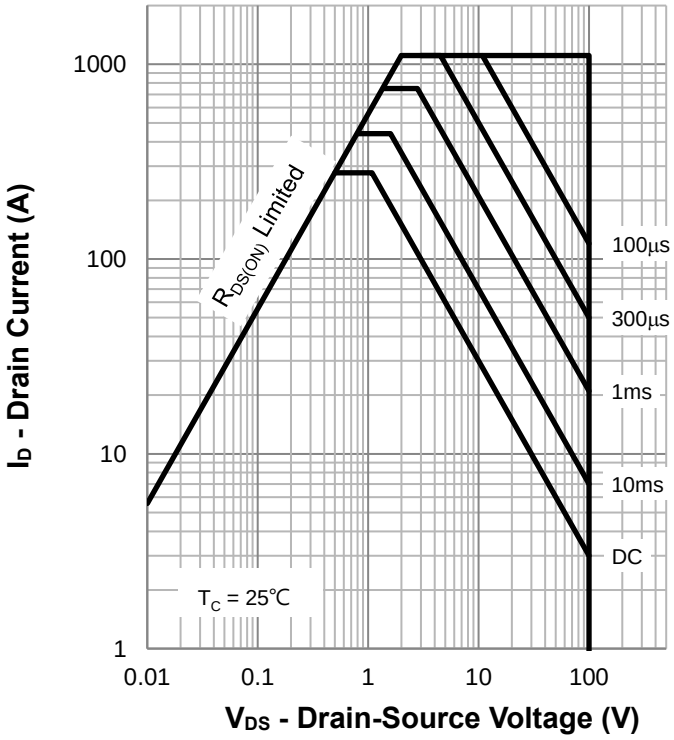
**Power Capability**



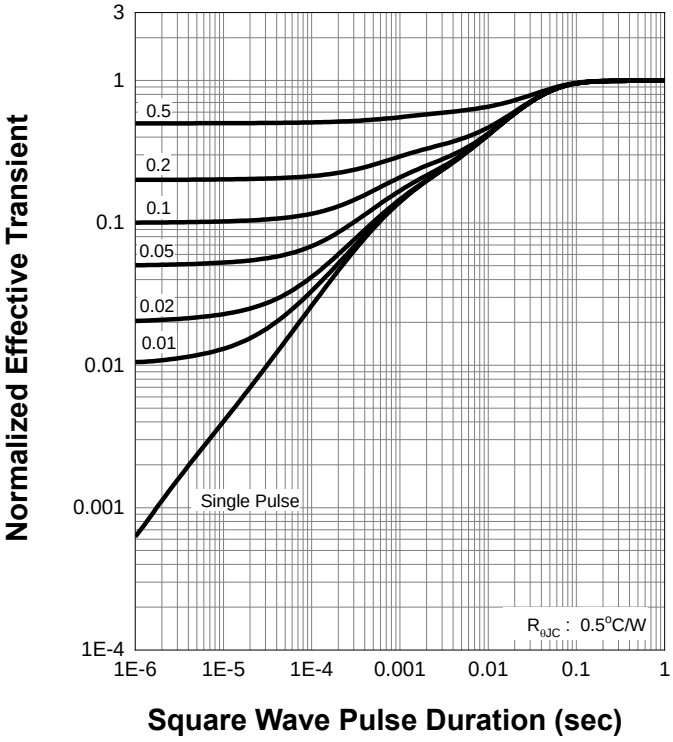

**Current Capability**




**Safe Operating Area**




**Transient Thermal Impedance**

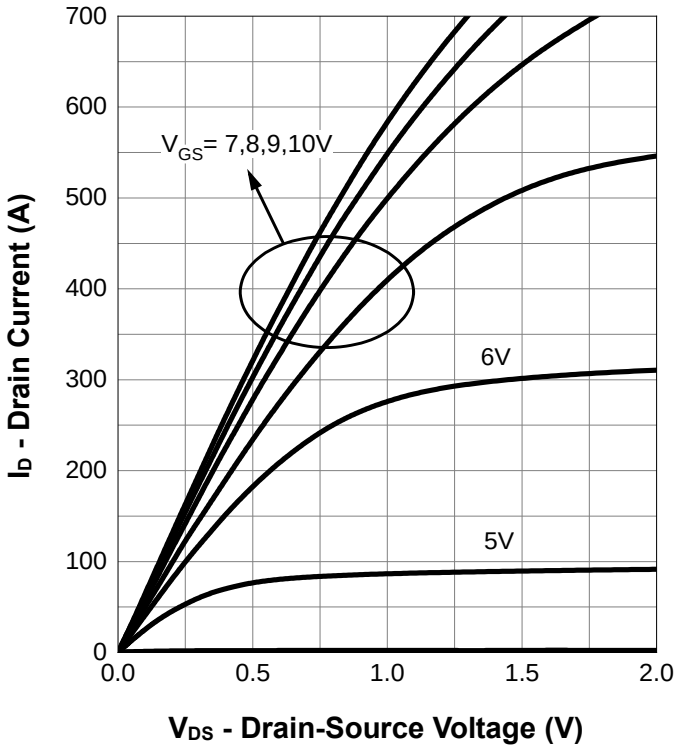




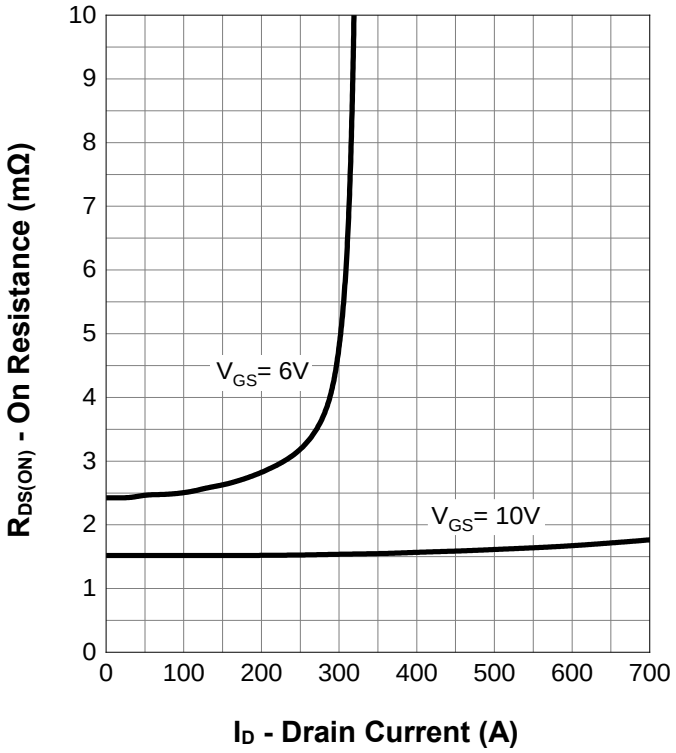

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## 6. Typical Characteristics

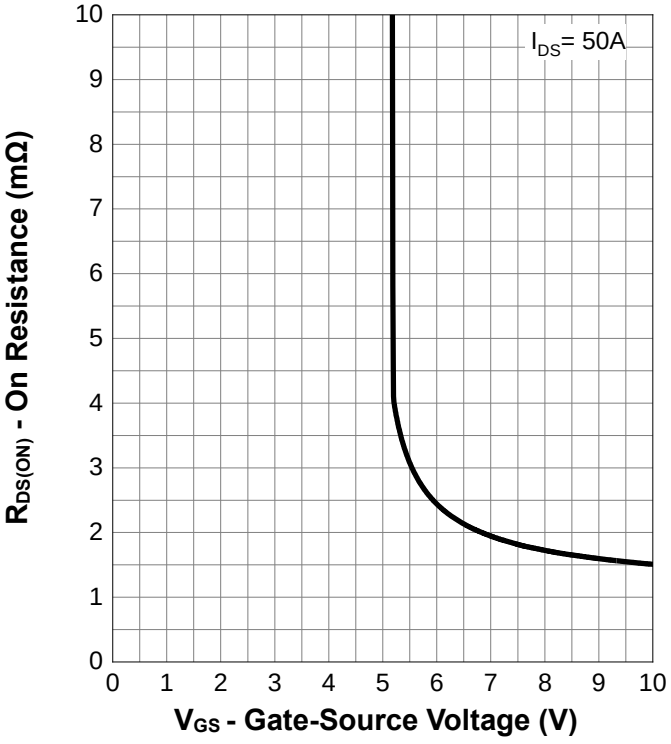
**Output Characteristics**



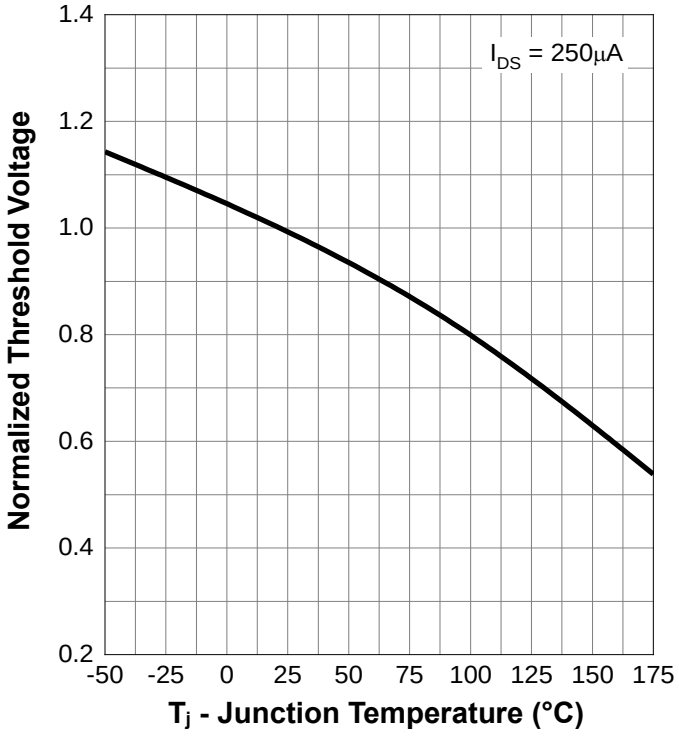

**On Resistance**




**Transfer Characteristics**




**Normalized Threshold Voltage**

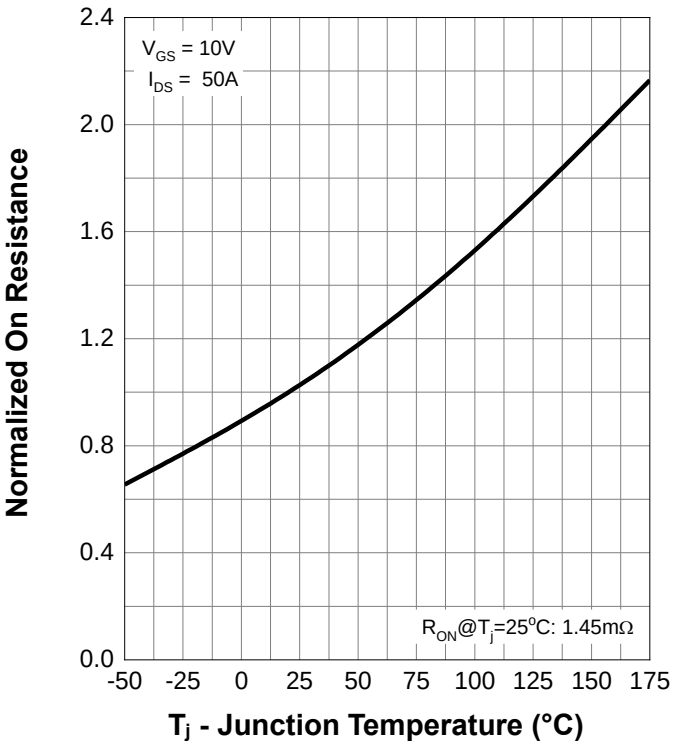




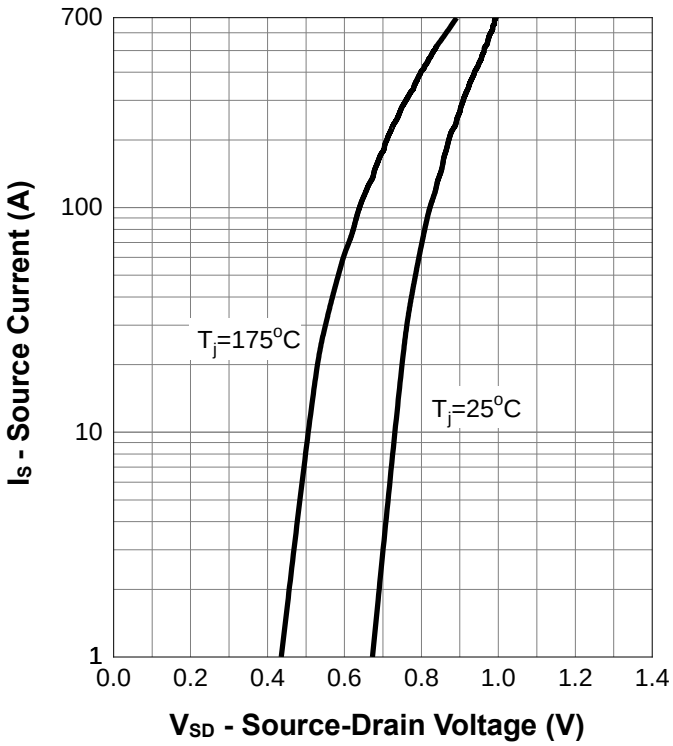
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## 6. Typical Characteristics

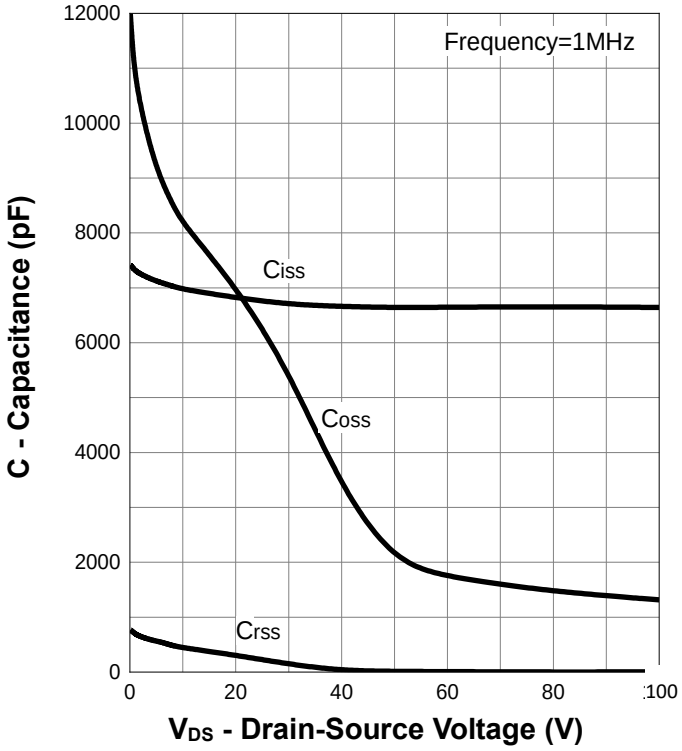
**Normalized On Resistance**



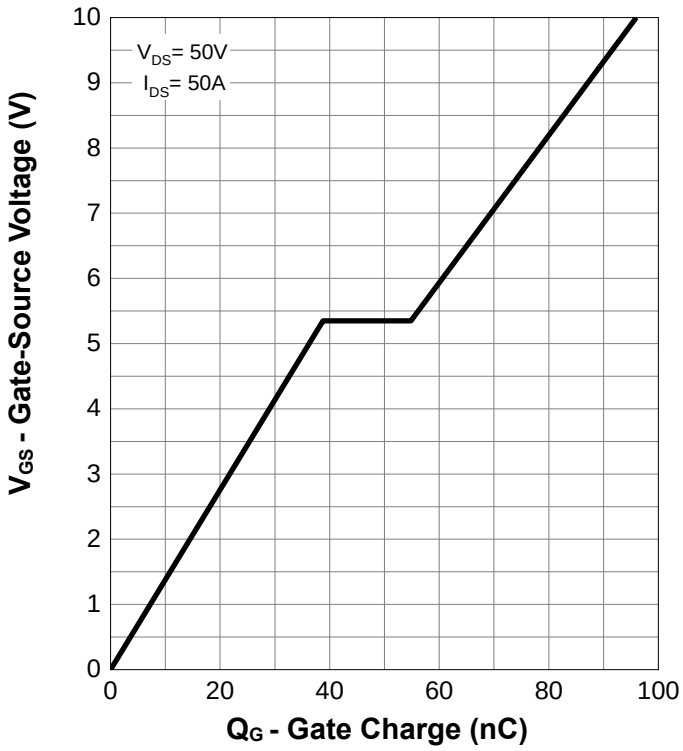

**Diode Forward Current**




**Capacitance**




**Gate Charge**





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