iM1824

P-Channel Enhancement Mode MOSFET

Datasheet

1. Product Information

1.1 Features

- Advanced Trench Cell Design
- RoHS Compliant and Halogen Free
- Reliable and Rugged
- Moisture Sensitivity Level MSL1



1.2 Package Type

PDFN5X6A-EP1-8L-02

1.3 Quick Reference

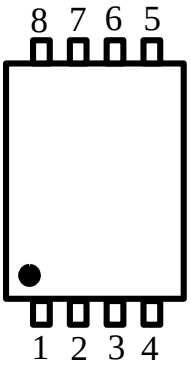
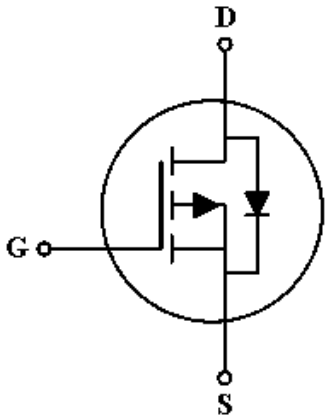
Symbol	Parameter	Conditions	Min	Typ	Max	Unit
Limiting Values						
P_{tot}	Total Power Dissipation	T_C = 25 ℃	-	-	68	W
I_D	Drain Current	T_C = 25 ℃, V_{GS} = -10 V	-	-	- 65	A
Static Characteristics						
BV_{DSS}	Drain-Source Breakdown Voltage	V_{GS} = 0 V, I_{DS} = - 250 μA	- 40	-	-	V
$V_{GS(th)}$	Gate Threshold Voltage	V_{DS} = V_{GS}, I_{DS} = - 250 μA	- 1	-	- 2	V
$R_{DS(ON)}$	On-State Resistance	V_{GS} = - 10 V, I_{DS} = - 15 A	-	7.2	9.0	mΩ
		V_{GS} = - 4.5 V, I_{DS} = - 8 A	-	10	13.5	

2. Ordering Code & Marking Information

Ordering Code	Marking Information
iM1824	iM1824 XXXXX X : Date Code

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3. Pin Description

Pin	Description	Simplified Outline	Symbol
1~3	Source(S)		
4	Gate(G)		
5~6	Drain(D)		

4. Limiting Values

Symbol	Parameter		Rating	Unit
V_{DS}	Drain-Source Voltage		- 40	V
V_{GS}	Gate-Source Voltage		± 20	V
I_D	Drain Current	T_C = 25 ℃, V_{GS} = - 10 V	- 65	A
		T_C = 100 ℃, V_{GS} = - 10 V	- 46	A
I_{DM} Note1	Pulsed Drain Current	T_C = 25 ℃, V_{GS} = - 10 V	- 260	A
I_S	Diode Forward Current	T_C = 25 ℃, V_{GS} = 0 V	- 65	A
E_{AS}	Single Pulsed Avalanche Energy	L = 1.0 mH, V_{DD} = - 40 V	264	mJ
P_{tot}	Total Power Dissipation	T_C = 25 ℃	68	W
$R_{\theta JA}$ Note2	Thermal Resistance- Junction to Ambient		58	℃ / W
$R_{\theta JC}$	Thermal Resistance- Junction to Case		2.2	℃ / W
T_{stg}	Storage Temperature		- 55 ~ 175	℃
T_J	Junction Temperature		175	℃

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5. Electrical Characteristics (T_A = 25 °C Unless Otherwise Noted)

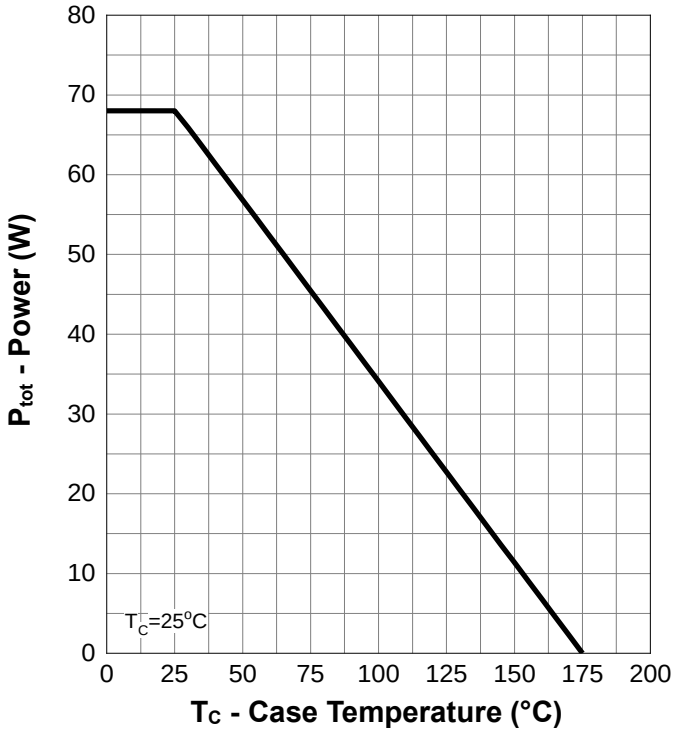
Symbol	Parameter	Conditions	Min	Typ	Max	Unit
Static Characteristics						
BV_{DSS}	Drain-Source Breakdown Voltage	V_{GS} = 0 V, I_{DS} = - 250 μA	- 40	-	-	V
$V_{GS(th)}$	Gate Threshold Voltage	V_{DS} = V_{GS}, I_{DS} = - 250 μA	- 1	-	- 2	V
I_{DSS}	Drain Leakage Current	V_{DS} = - 32 V, V_{GS} = 0 V	-	-	- 1	μA
I_{GSS}	Gate Leakage Current	V_{DS} = 0 V, V_{GS} = ± 20 V	-	-	± 100	nA
$R_{DS(ON)}$	On-State Resistance	V_{GS} = - 10 V, I_{DS} = - 15 A	-	7.2	9.0	mΩ
		V_{GS} = - 4.5 V, I_{DS} = - 8 A	-	10	13.5	
Diode Characteristics						
V_{SD}	Diode Forward Voltage	I_{SD} = - 15 A, V_{GS} = 0 V	-	-	1.3	V
t_{rr} Note3	Reverse Recovery Time	I_{DS} = - 15 A, V_{GS} = 0 V	-	20	-	nS
Q_{rr} Note3	Reverse Recovery Charge	dI_{SD}/dt = 100 A/μs	-	13	-	nC
Dynamic Characteristics Note3						
C_{iss}	Input Capacitance	V_{GS} = 0 V, V_{DS} = -20 V Frequency = 1 MHz	-	6102	-	pF
C_{oss}	Output Capacitance		-	403	-	
C_{rss}	Reverse Transfer Capacitance		-	288	-	
t_d(on)	Turn-on Delay Time	V_{DS} = -20 V, V_{GEN} = - 10 V, R_G = 3.9 Ω, R_L = 1.33 Ω, I_{DS} = - 15 A	-	10	-	nS
t_r	Turn-on Rise Time		-	36	-	
t_d(off)	Turn-off Delay Time		-	140	-	
t_f	Turn-off Fall Time		-	62	-	
Gate Charge Characteristics Note3						
Q_g	Total Gate Charge	V_{DS} = - 20 V, V_{GS} = - 10 V, I_{DS} = - 15 A	-	98	-	nC
Q_{gs}	Gate-Source Charge		-	23	-	
Q_{gd}	Gate-Drain Charge		-	13	-	

Note 1：Pulse test; pulse width ≤ 300 μs, duty cycle ≤ 2%.
Note 2：Surface Mounted on 1 in^2 pad area, t ≤ 10 sec.
Note 3：Guaranteed by design, not subject to production testing.

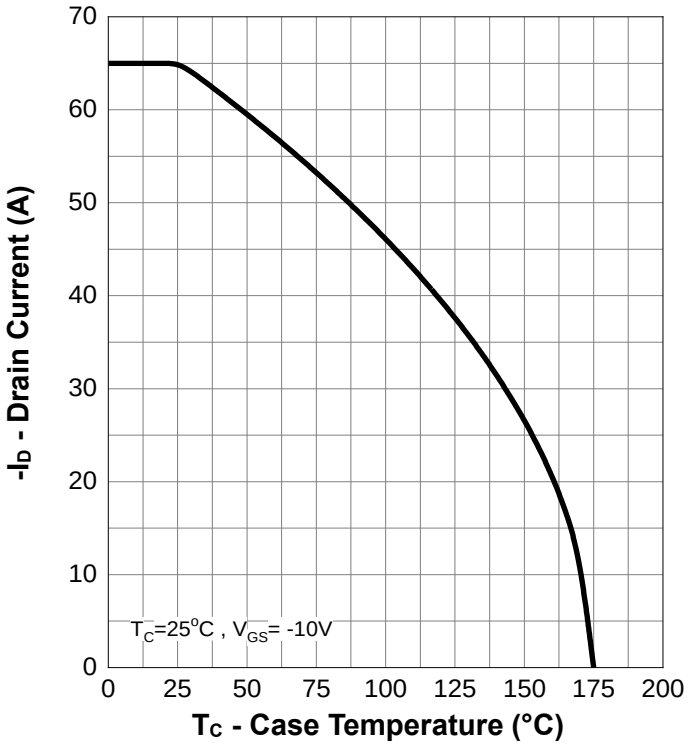
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6. Typical Characteristics

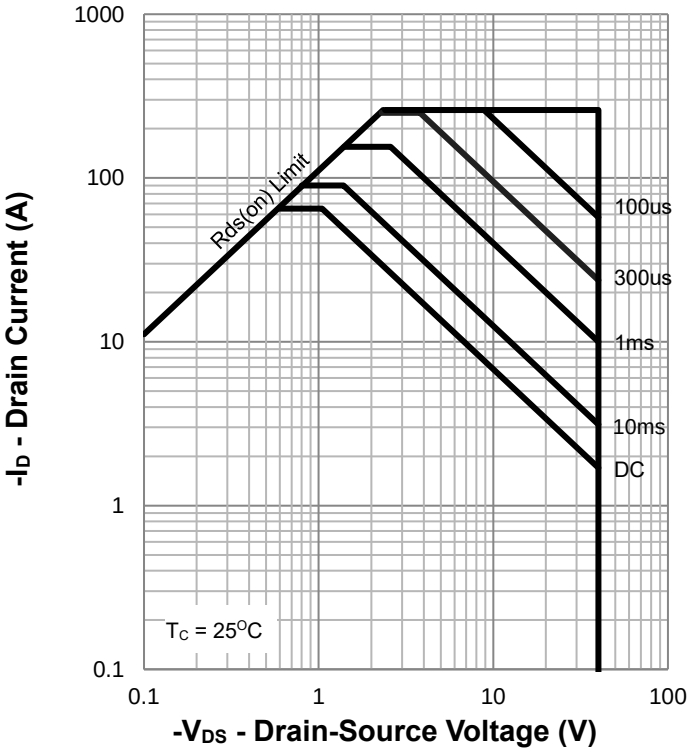
Power Capability



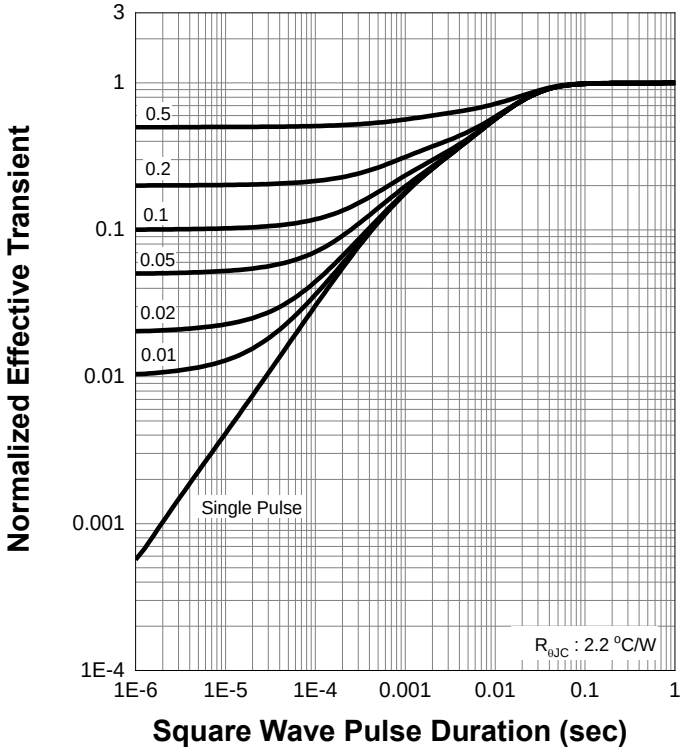
Current Capability



Safe Operating Area



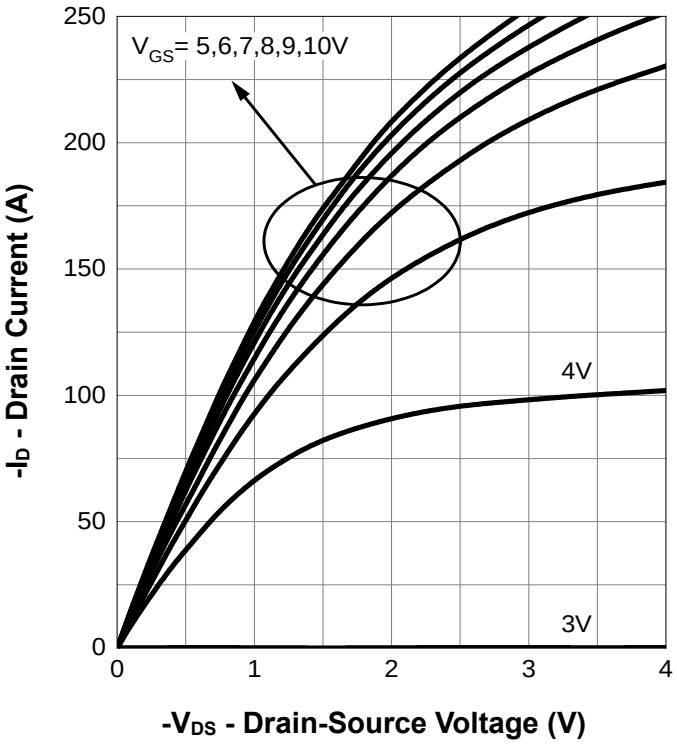
Transient Thermal Impedance



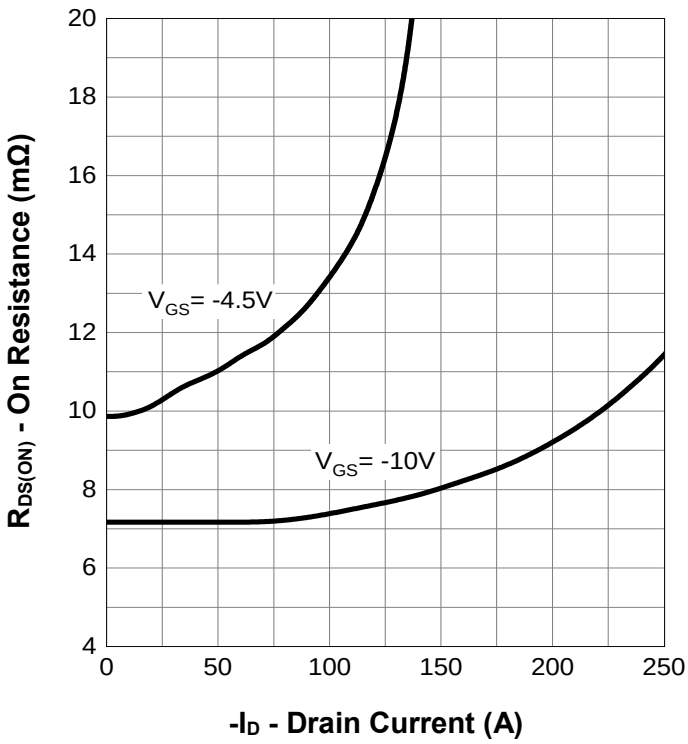
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6. Typical Characteristics

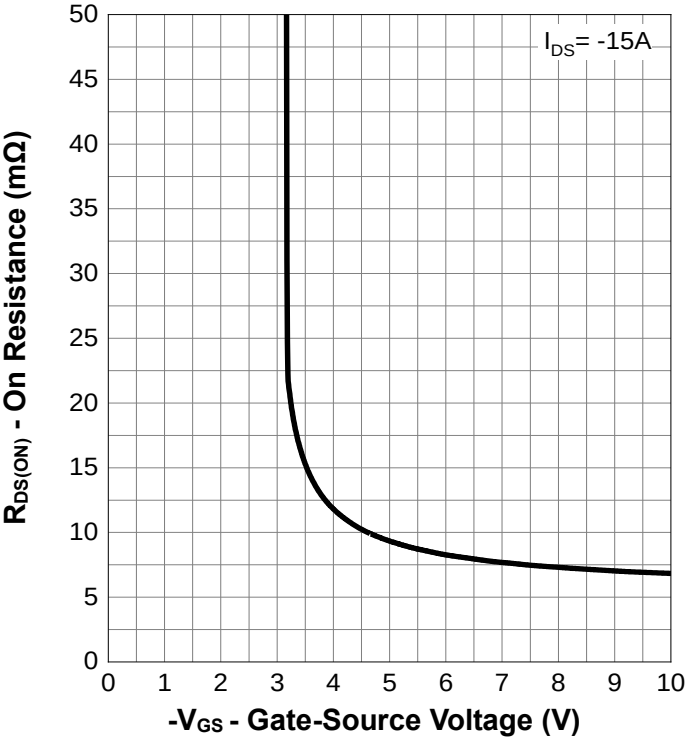
Output Characteristics



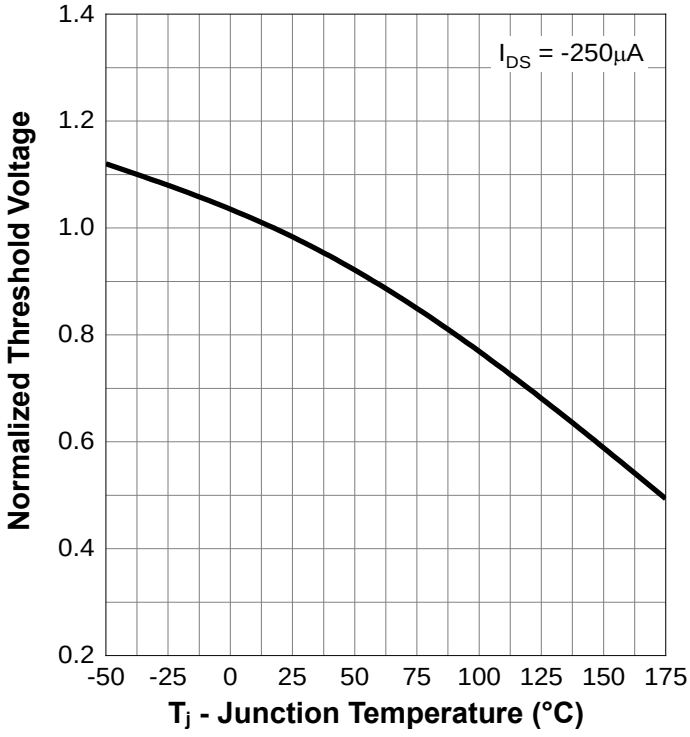
On Resistance



Transfer Characteristics



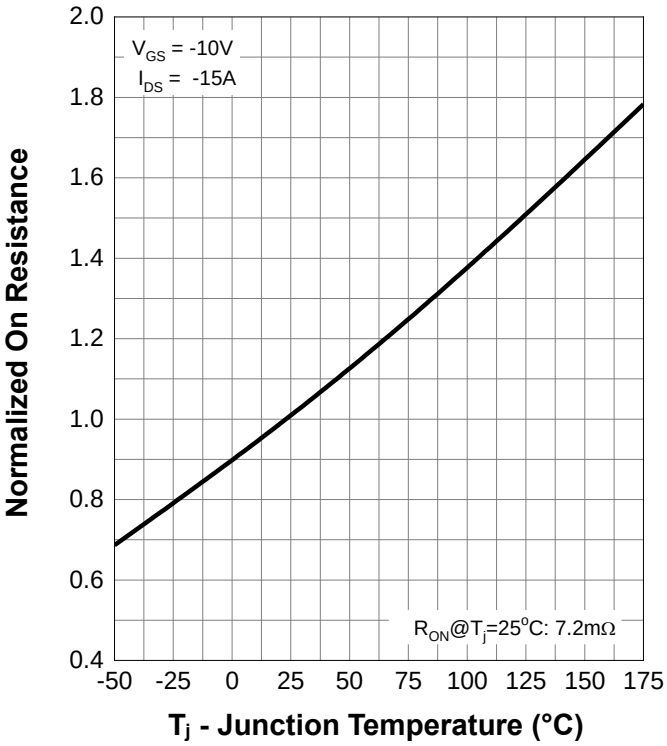
Normalized Threshold Voltage



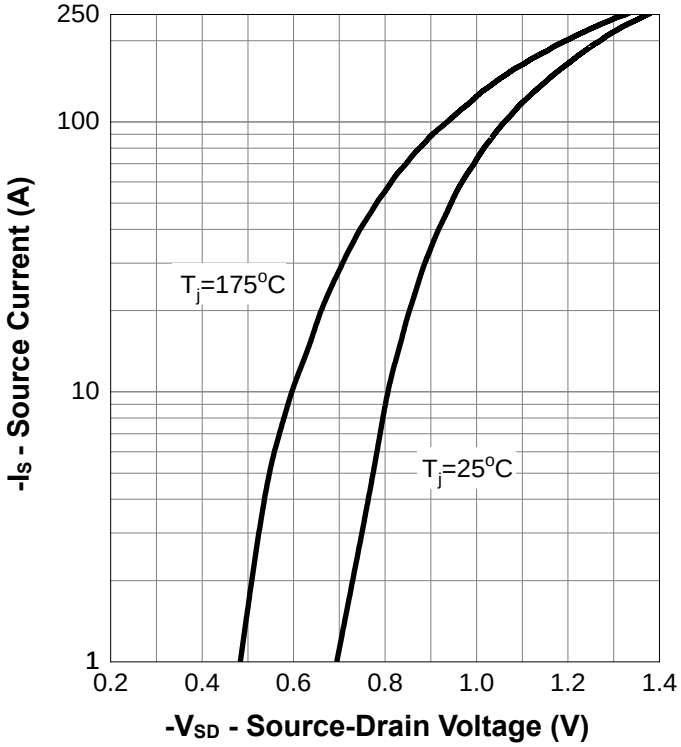
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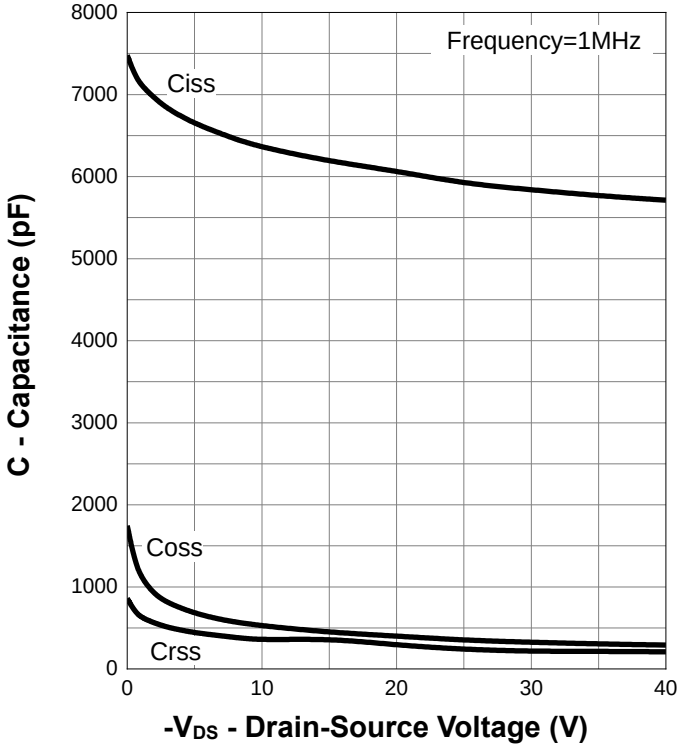
Normalized On Resistance



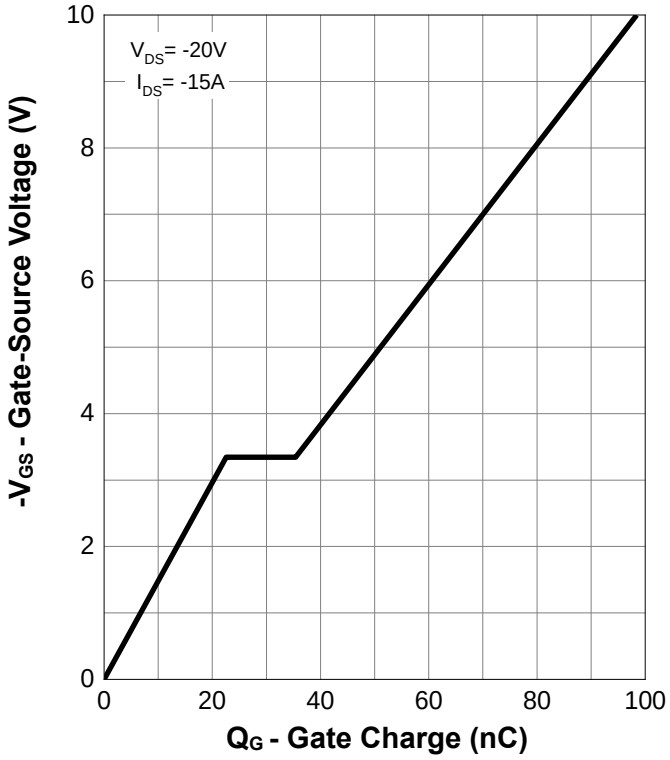
Diode Forward Current



Capacitance



Gate Charge



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